

Attorney Docket No. 5649-1192


PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re: Park et al.

U.S. Patent No.: 7,148,541

Application Serial No.: 10/780,067

Issued: December 12, 2006

Filed: February 17, 2004

For: *Vertical Channel Field Effect Transistors having Insulating Layers Thereon*

Date: February 20, 2007

Commissioner for Patents

Attn: Certificate of Correction Branch

P.O. Box 1450

Alexandria, VA 22313-1450

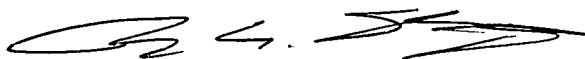
**REQUEST FOR ENTRY OF CERTIFICATE OF CORRECTION UNDER
35 U.S.C §254 AND 37 C.F.R. §1.322**

Sir:

The Assignee of record for the above-referenced patent hereby requests, pursuant to 35 U.S.C §254 and 37 C.F.R. §1.322, that a Certificate of Correction be issued. This request is made in order to correct the mistakes incurred through the fault of the U.S. Patent and Trademark Office. No fee is believed due. However, the Commissioner is authorized to charge any deficiency or credit any overpayment to Deposit Account No. 50-0220.

The mistakes appearing in the patent are set forth with corrections on the Certificate of Correction enclosed herewith, with an additional copy thereof and a return post card.

Respectfully submitted,



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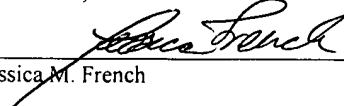
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**Certificate
FEB 27 2007
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I hereby certify that this correspondence is being deposited with the United States Postal Service with sufficient postage as first class mail in an envelope addressed to: Commissioner for Patents, Attn: Certificate of Correction Branch, P.O. Box 1450, Alexandria, VA 22313-1450 on February 20, 2007.


Jessica M. French

FEB 28 2007

UNITED STATES PATENT AND TRADEMARK OFFICE
CERTIFICATE OF CORRECTION

PATENT NO. : 7,148,541
DATED : December 12, 2006
INVENTOR(S) : Park et al.

It is certified that error appears in the above-identified patent and that said Letters Patent is hereby corrected as shown below:

Column 12,

Line 12 should read -- a gate insulating layer that are formed on upper sidewalls --

Line 23 should read -- is uniform, and a lower width of the fin coated with the --

Column 14,

Line 9 should read -- source and drain regions formed in the pillar at both sides --

MAILING ADDRESS OF SENDER:
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PATENT NO. 7,148,541
No. of additional copies: _____

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